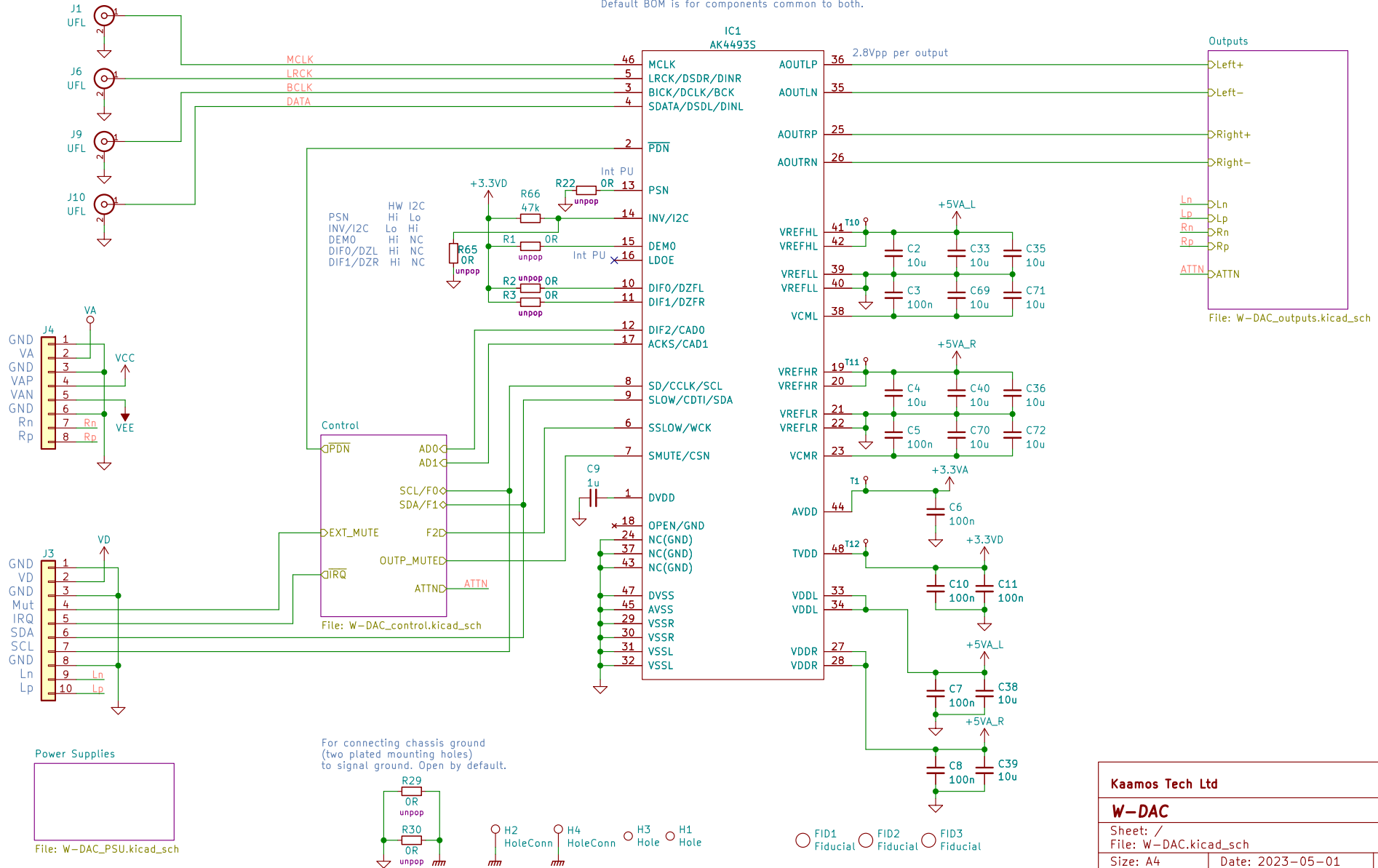


W-DAC v2.1

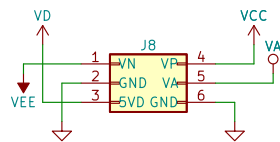
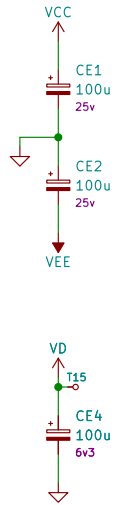
Root sheet

Two versions: A) HW-control B) I2C control.
Default BOM is for components common to both.

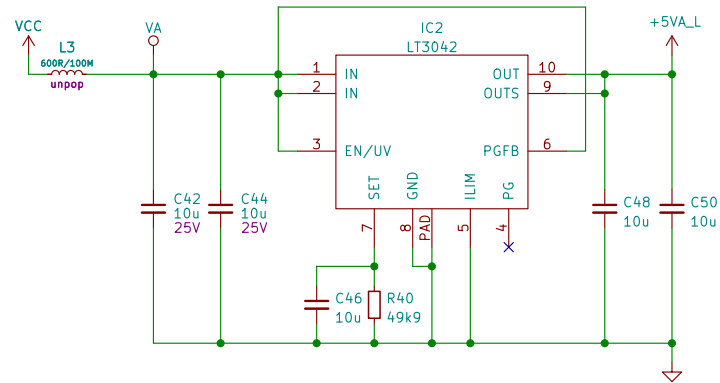


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W-DAC		
Sheet: /		
File: W-DAC.kicad_sch		
Size: A4	Date: 2023-05-01	Rev: v2.1
KiCad E.D.A.	kicad (6.0.9)	Id: 1/4

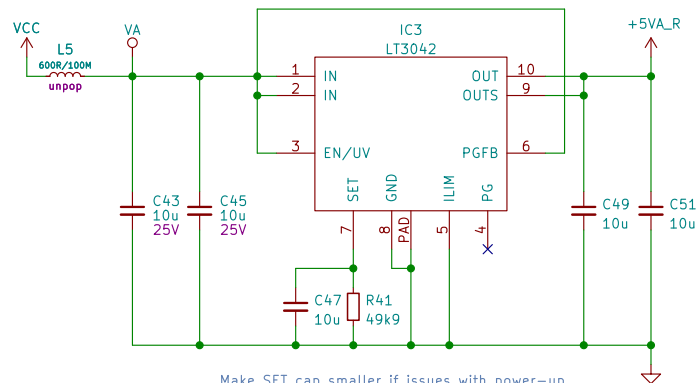
Power Supplies



Analog supplies

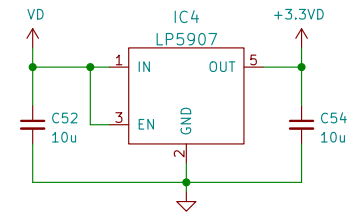


If VA is not available, VCC can be used by populating L3/L5. Make sure VA is not connected to any other board or PSU that may get damaged from high VA!

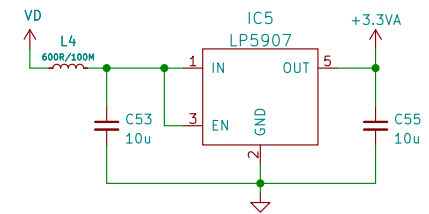


Make SET cap smaller if issues with power-up due to very slow settling of 5VA supplies.

DVDD Supply



AVDD Supply (clock interface supply)



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W-DAC

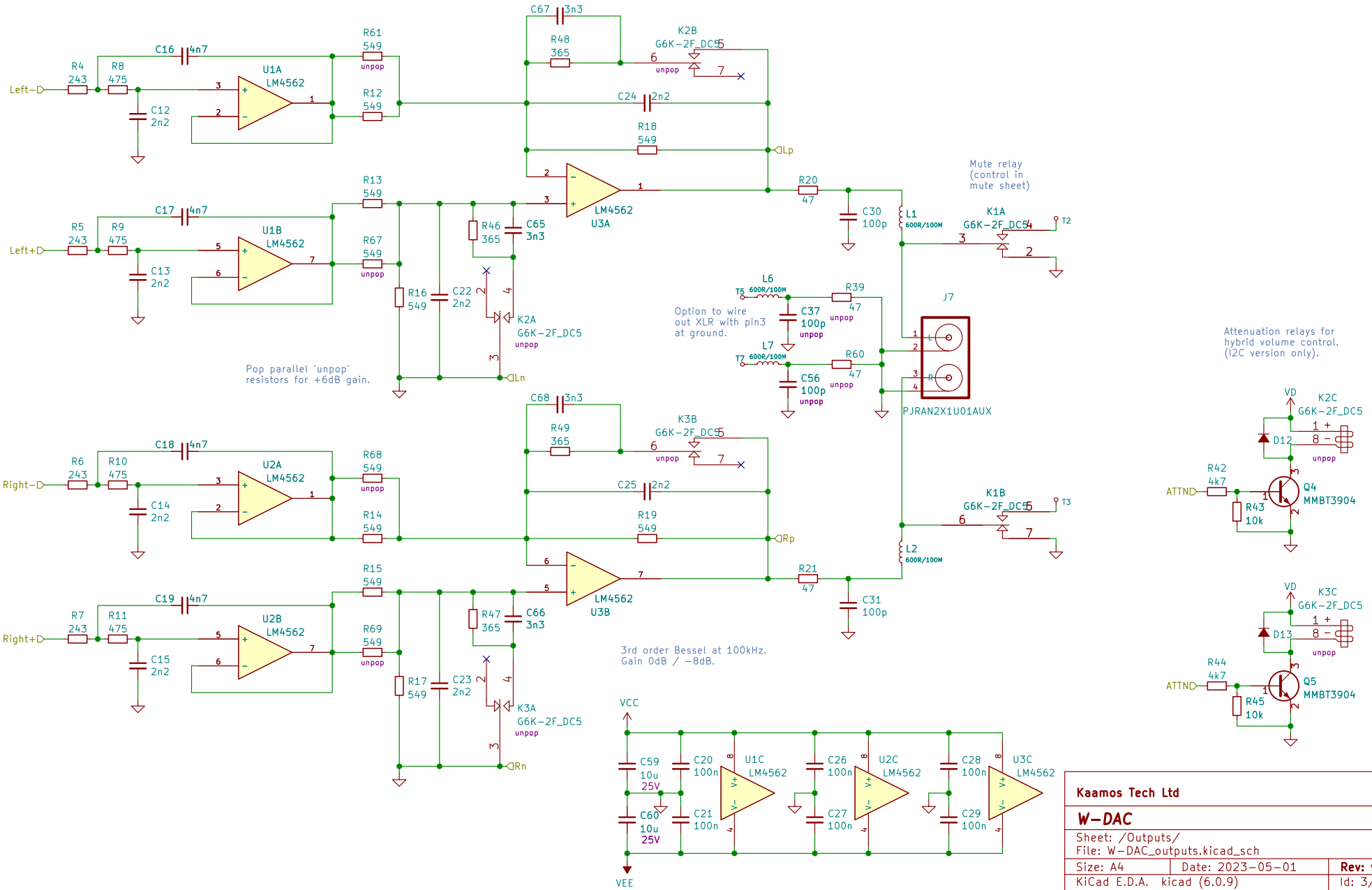
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File: W-DAC_PSU.kicad_sch

Size: A4 Date: 2023-05-01 Rev: v2.1

KiCad E.D.A. kicad (6.0.9)

Id: 2/4

Outputs



Kaamos Tech Ltd	
W-DAC	
Sheet: /Outputs/ File: W-DAC_outputs.kicad_sch	
Size: A4	Date: 2023-05-01
KiCad E.D.A.	kicad (6.0.9)
Rev: v2.1	Id: 3/4

Control

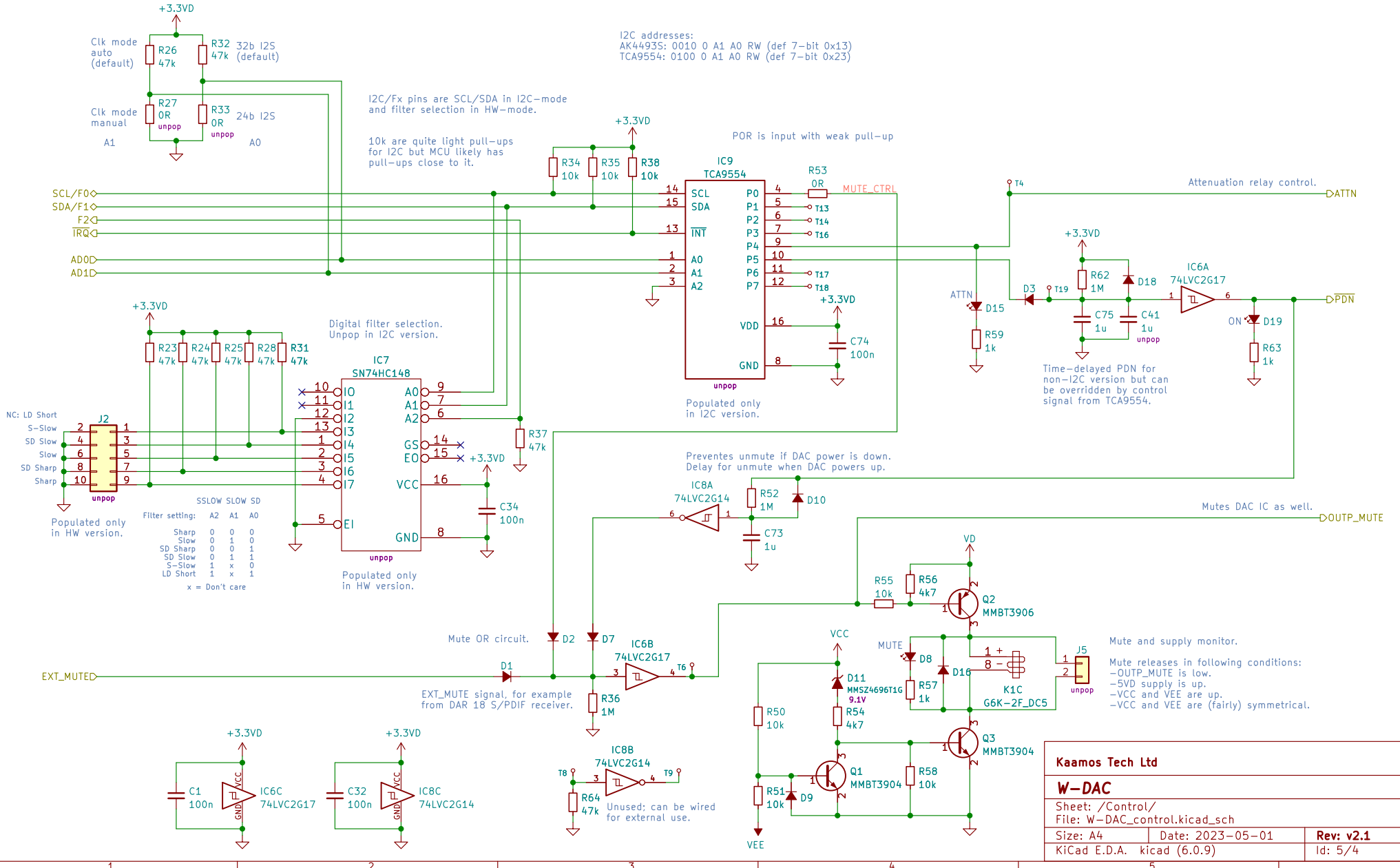
Clocking and data in HW-mode,
address pins in I2C-mode.

I2C addresses:
AK4493S: 0010 0 A1 A0 RW (def 7-bit 0x13)
TCA9554: 0100 0 A1 A0 RW (def 7-bit 0x23)

I2C/Fx pins are SCL/SDA in I2C-mode
and filter selection in HW-mode.

10k are quite light pull-ups
for I2C but MCU likely has
pull-ups close to it.

POR is input with weak pull-up



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W-DAC	
Sheet: /Control/	
File: W-DAC_control.kicad_sch	
Size: A4	Date: 2023-05-01
KiCad E.D.A.	Rev: v2.1
kicad (6.0.9)	Id: 5/4