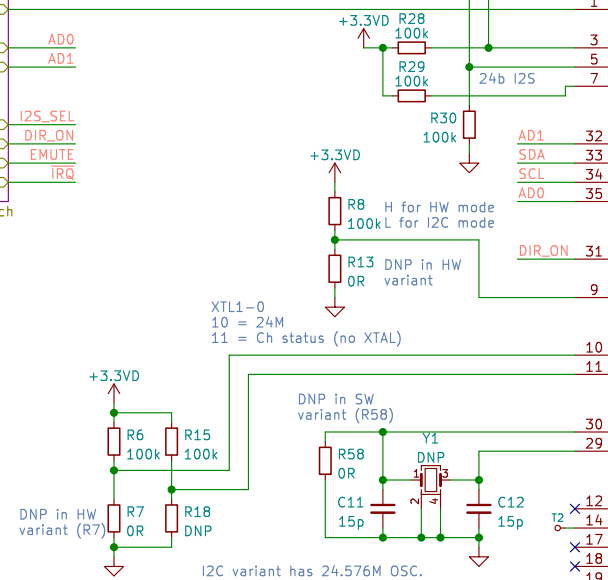
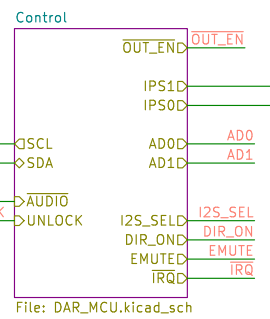
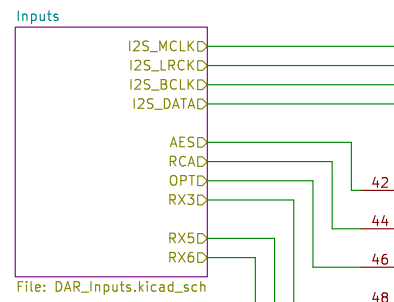
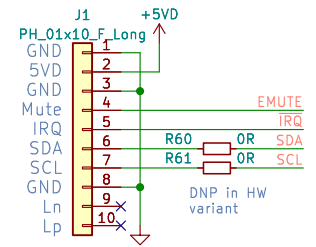
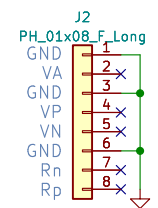


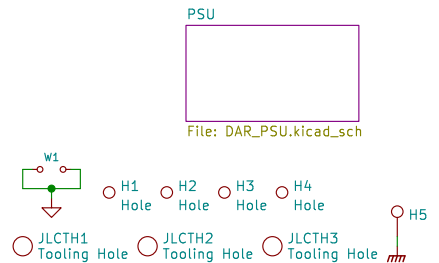
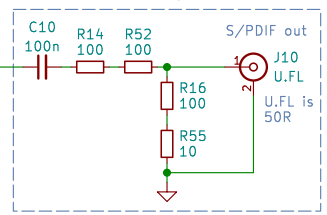
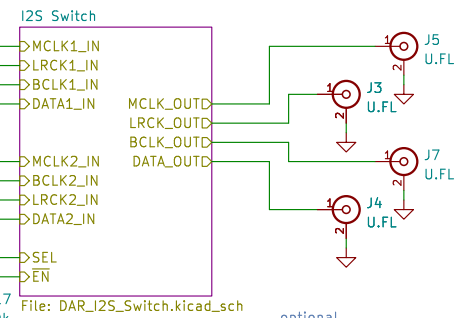
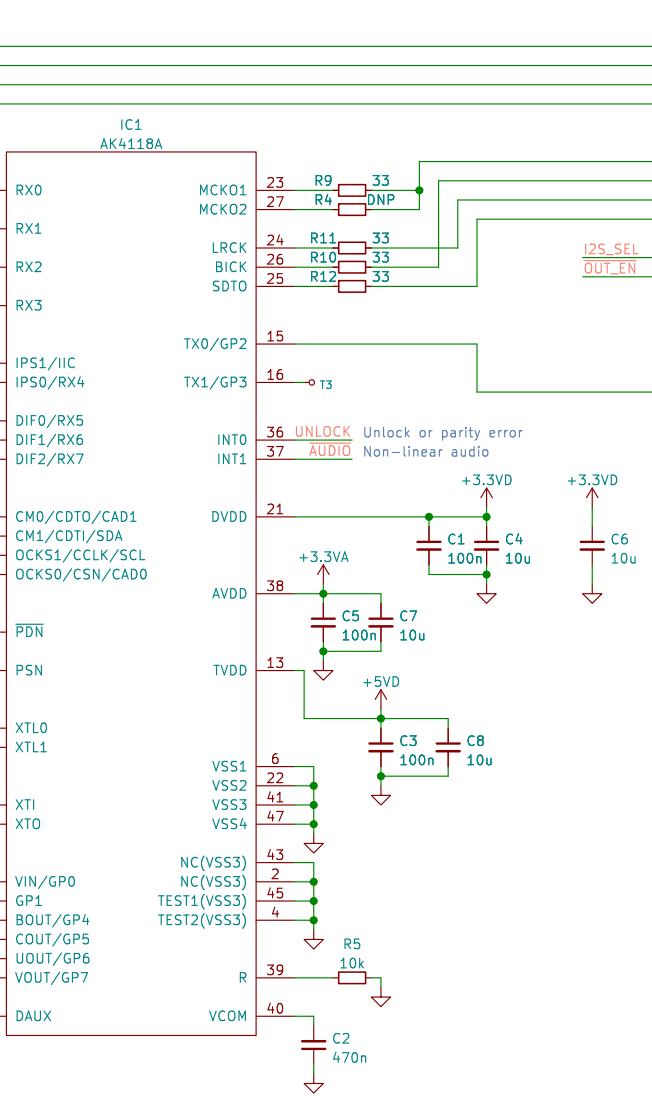
DAR 18 – Audio Receiver

Root sheet



I2C variant has 24.576M OSC. HW-variant grounds XTL1 via R58. This improves performance in HW. In I2C-variant OSC should be disabled when not needed.

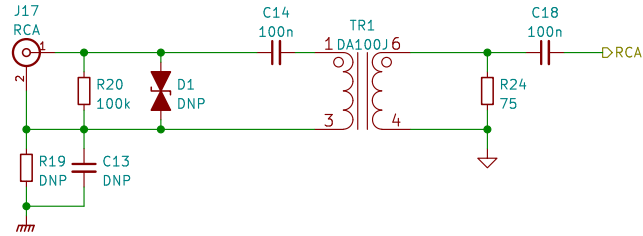
Without OSC I2S output is low-freq free-running PLL output which may cause issues to some DACs if ext Mute is not used. In this case OSC can be used (DNP R58, pop R7).



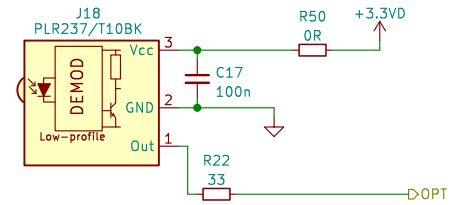
Kaamos Tech Ltd / Tomi Nihtila		
DAR 18 Audio Receiver		
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Size: A4	Date: 2023-08-19	Rev: v1.2
KiCad E.D.A.	kicad 7.0.5	Id: 1/5

Inputs

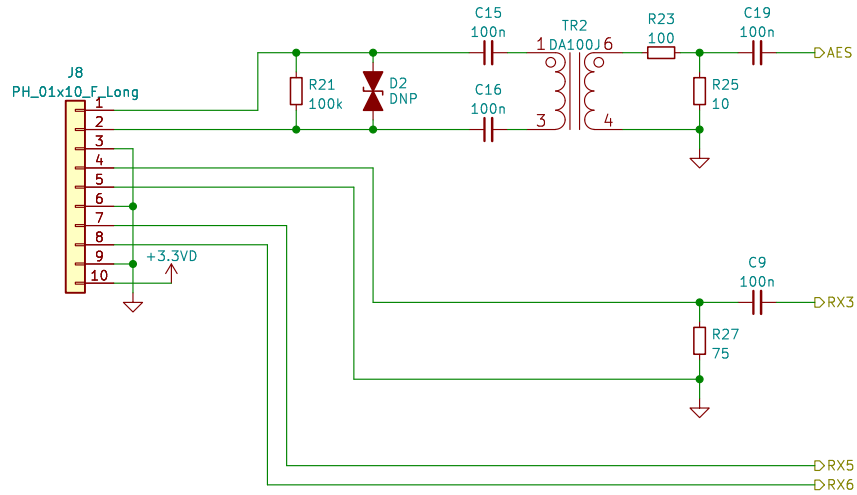
RCA input



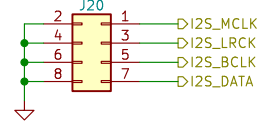
OPT input



XLR input
Not onboard connector, wire out to rear panel



PCM input



CMOS
Need external interface circuit

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DAR 18 Audio Receiver

Sheet: /Inputs/
File: DAR_Inputs.kicad_sch

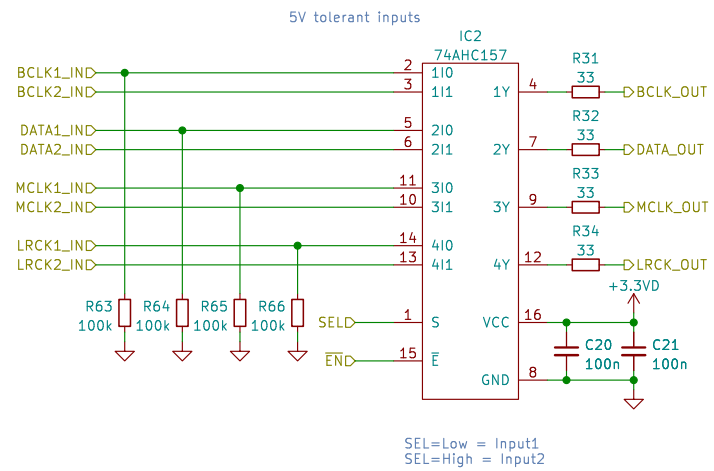
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Rev: v1.2

KiCad E.D.A. kicad 7.0.5

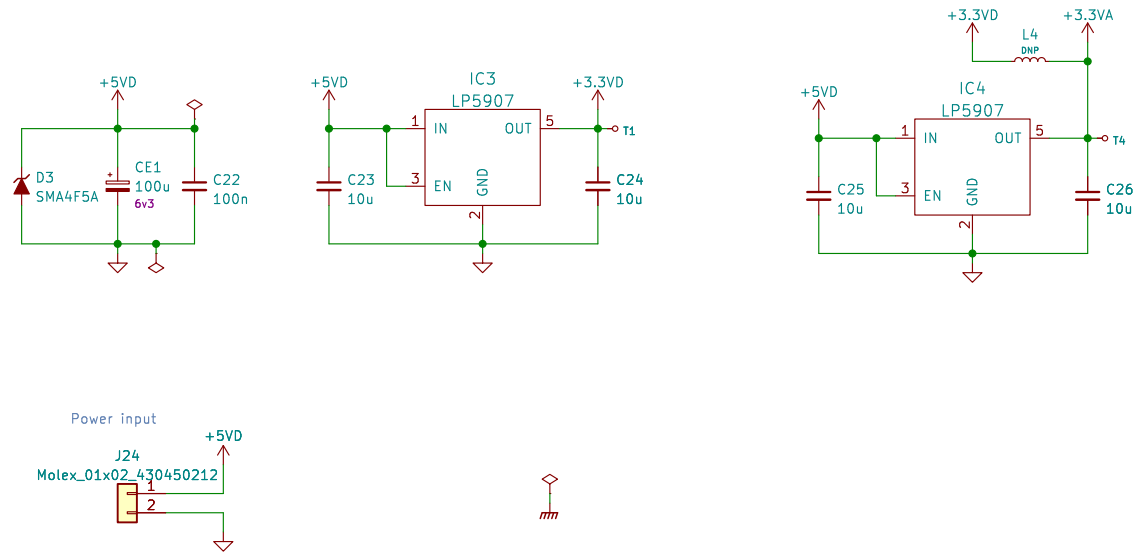
Id: 2/5

I2S Output Switch / Buffer



Kaamos Tech Ltd / Tomi Nihtila		
DAR 18 Audio Receiver		
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Size: A4	Date: 2023-08-19	Rev: v1.2
KiCad E.D.A.	kicad 7.0.5	Id: 3/5

Power Supply



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DAR 18 Audio Receiver

Sheet: /PSU/
File: DAR_PSU.kicad_sch

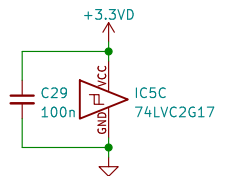
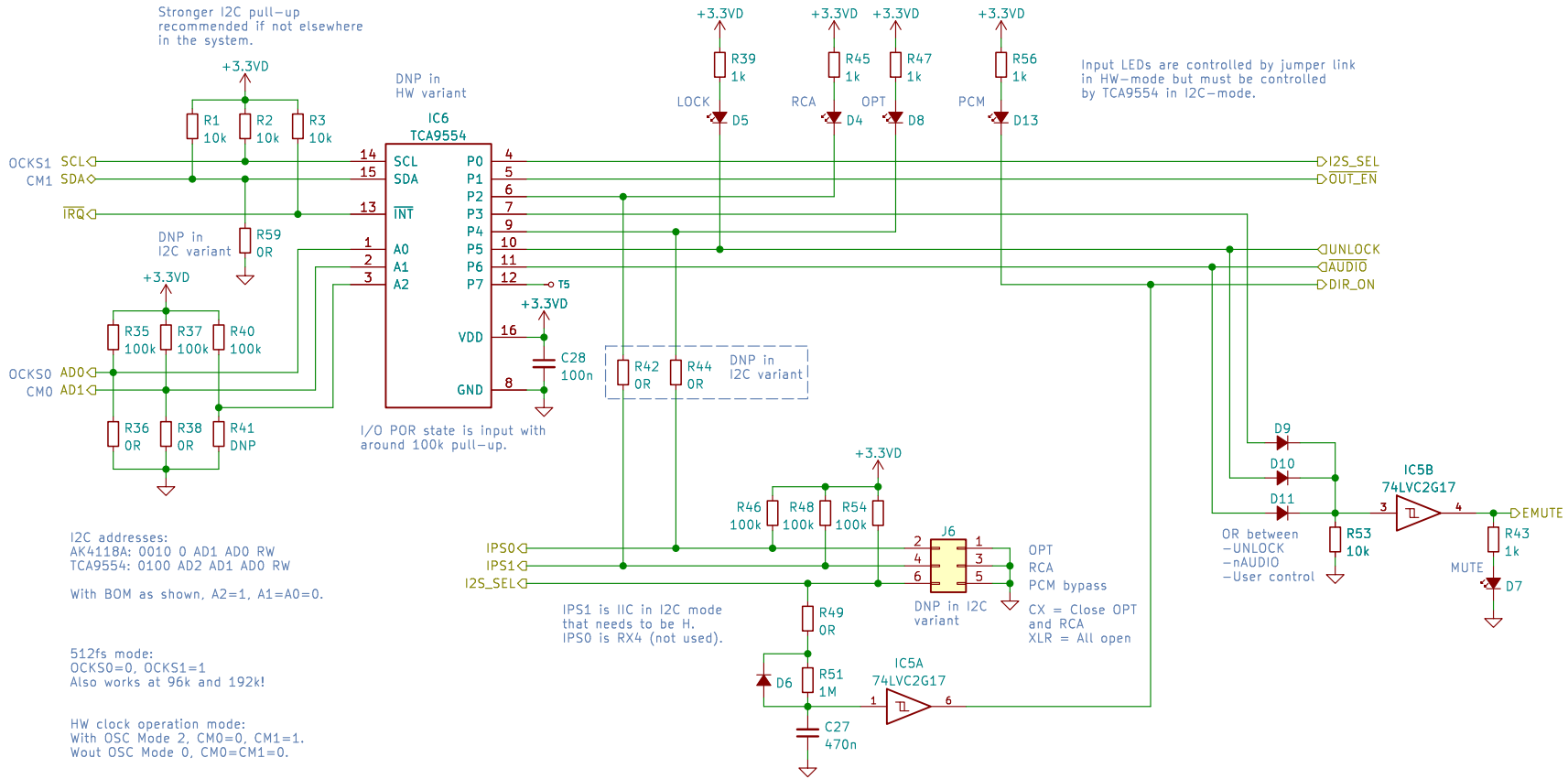
Size: A4 Date: 2023-08-19

Rev: v1.2

KiCad E.D.A. kicad 7.0.5

Id: 4/5

Control



Kaamos Tech Ltd / Tomi Nihtila		
DAR 18 Audio Receiver		
Sheet: /Control/ File: DAR_MCU.kicad_sch		
Size: A4	Date: 2023-08-19	Rev: v1.2
KiCad E.D.A.	kicad 7.0.5	Id: 7/5