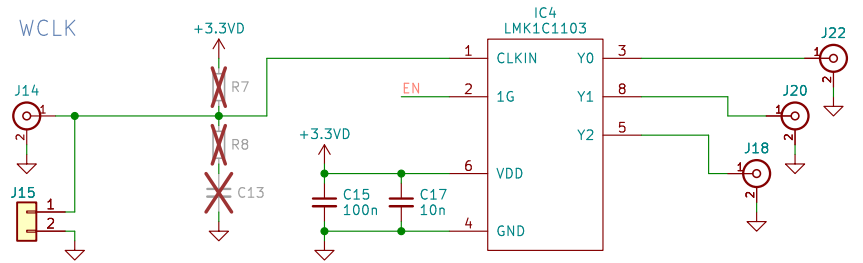
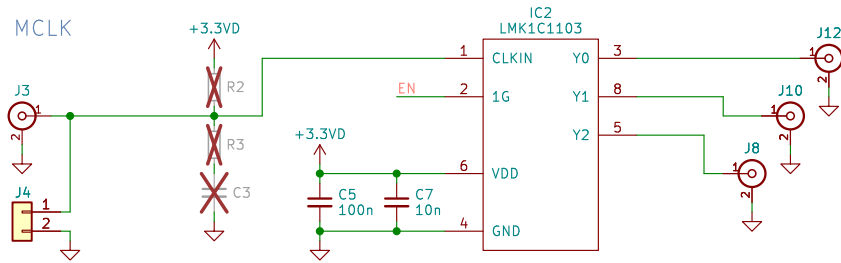
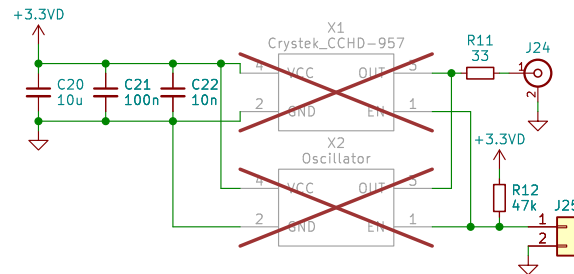
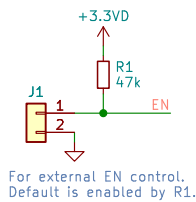
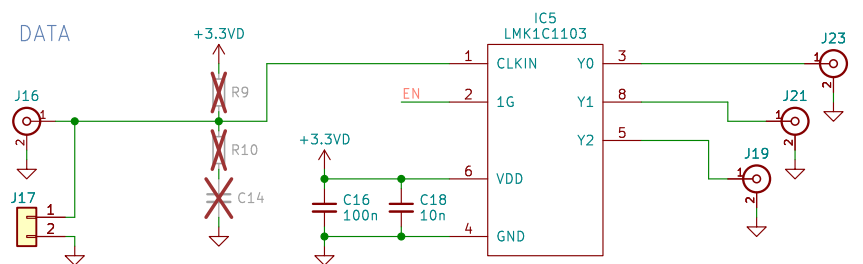
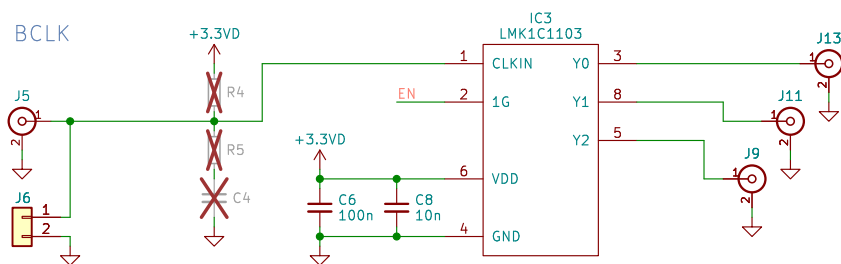


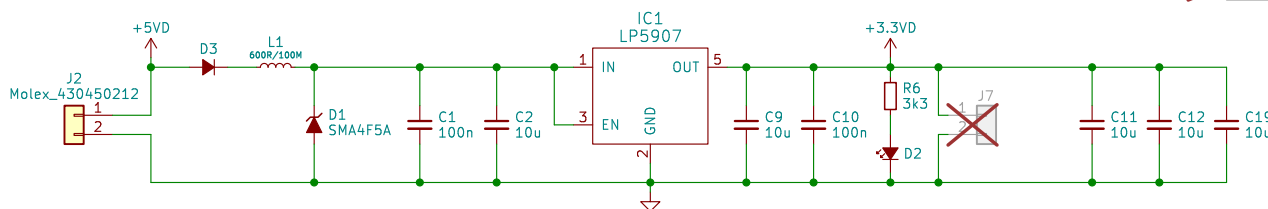
Clock Buffer 1-3 v1.0



Yx have 50R output impedance.
CLKIN and 1G have weak internal PD.



- JLC1H1 Tooling Hole
- JLC1H2 Tooling Hole
- JLC1H3 Tooling Hole
- H1 Hole
- H2 Hole
- H3 Hole
- H4 Hole



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Clock Buffer 1-3		
Sheet: /		
File: Clock_Buffer_1-3.kicad_sch		
Size: A4	Date: 2024-01-23	Rev: v1.0
KiCad E.D.A.	kicad 7.0.5	Id: 1/1